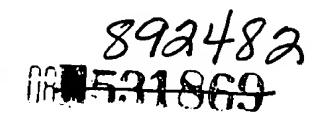


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ВУ	CLASS	SUBCLASS
DRAFTSMAN		



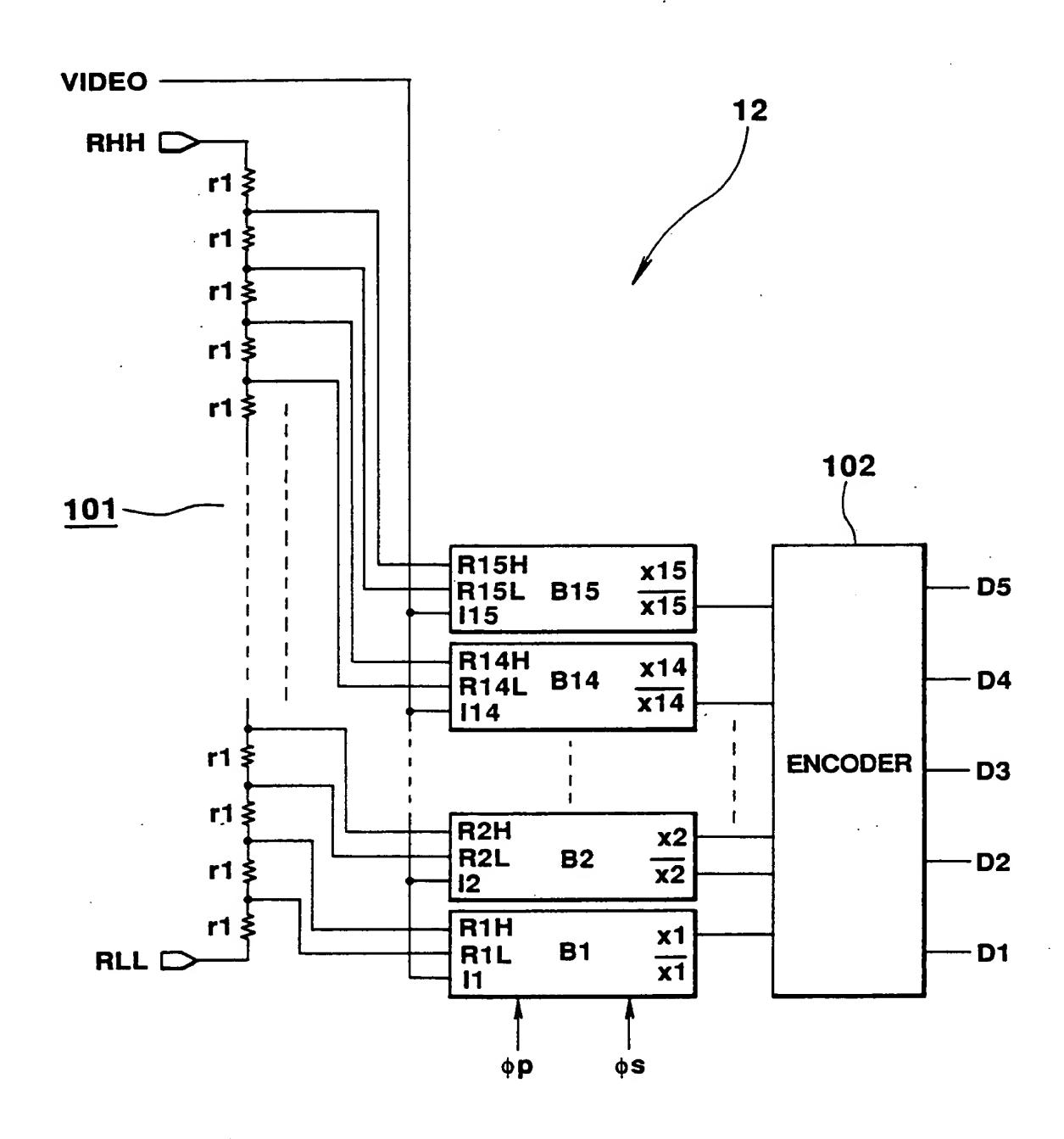
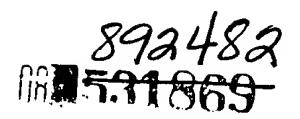


FIG.2

Carlotter March 1981	THE RESERVE OF THE PARTY OF THE						
A:>PROVED	O.G. FIG.						
BY	CLASS	SUBCLASS					
DRAFTSMAN							



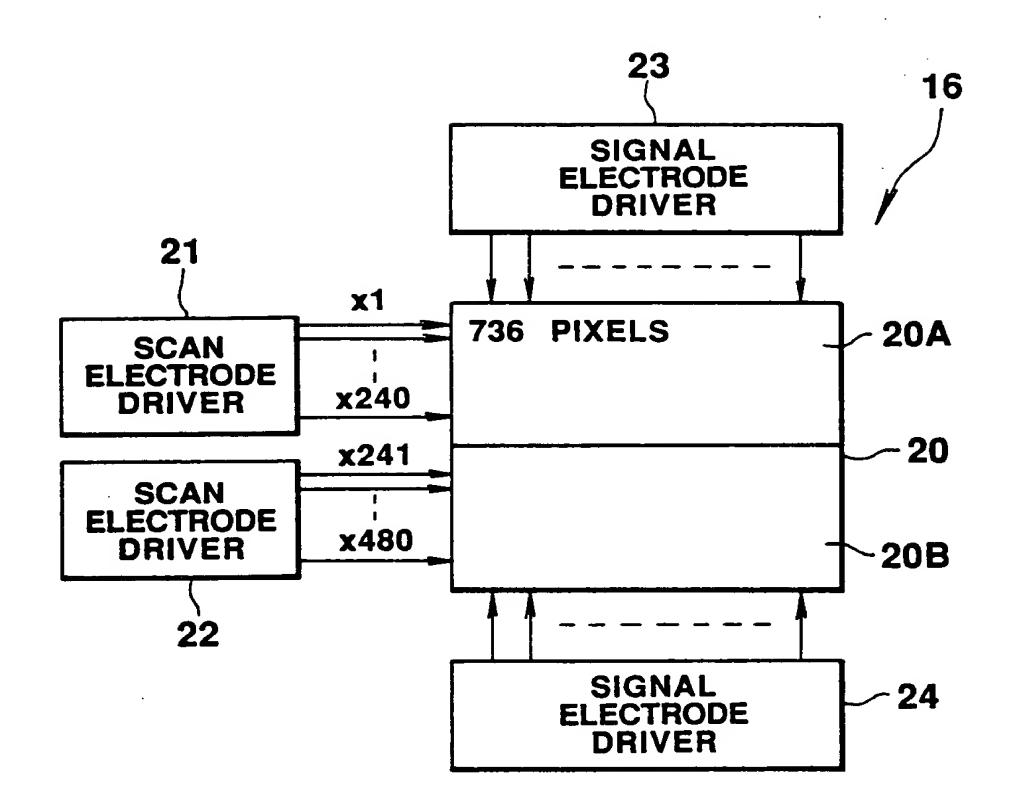


FIG.3

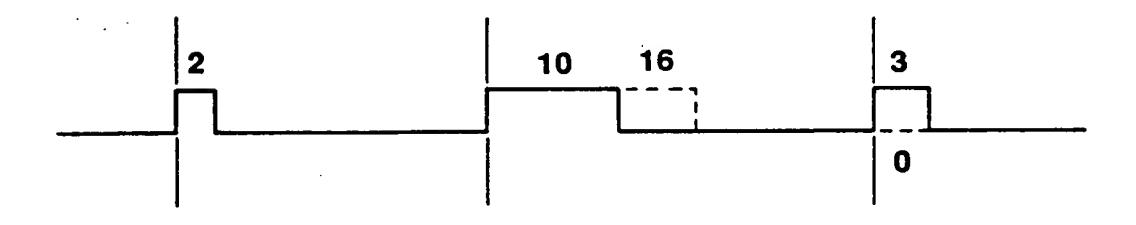
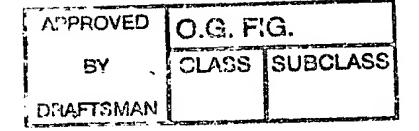
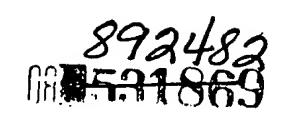


FIG.4





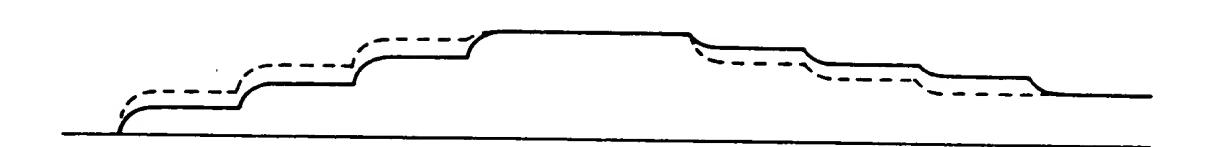


FIG.5

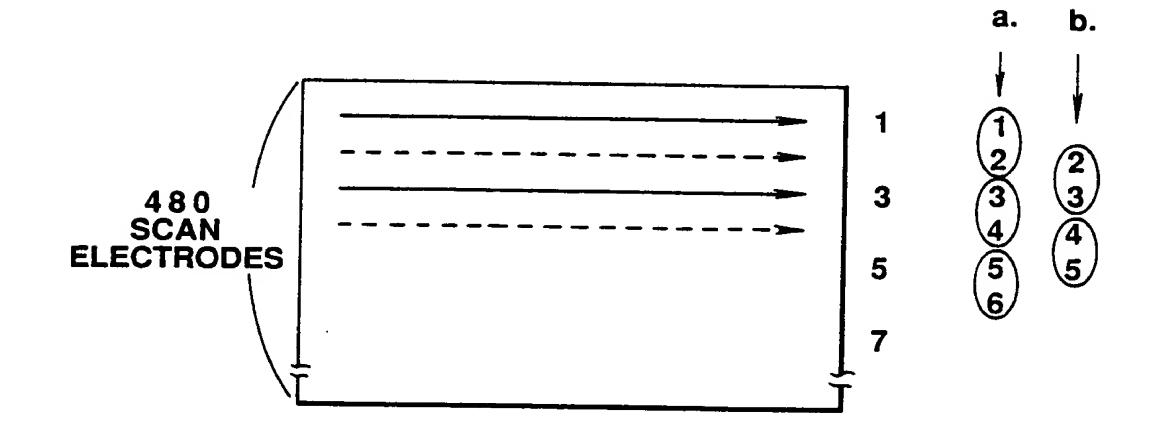
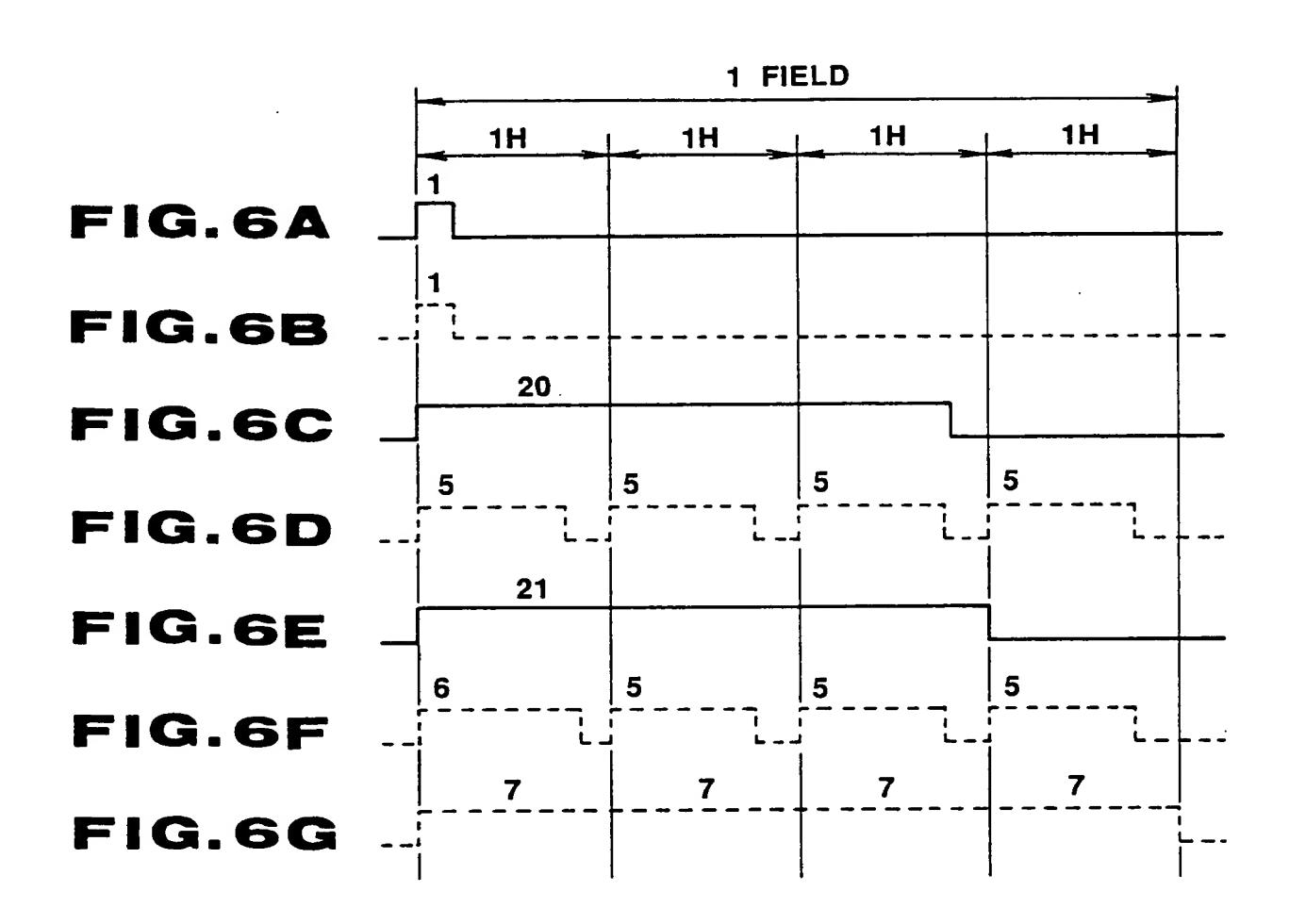
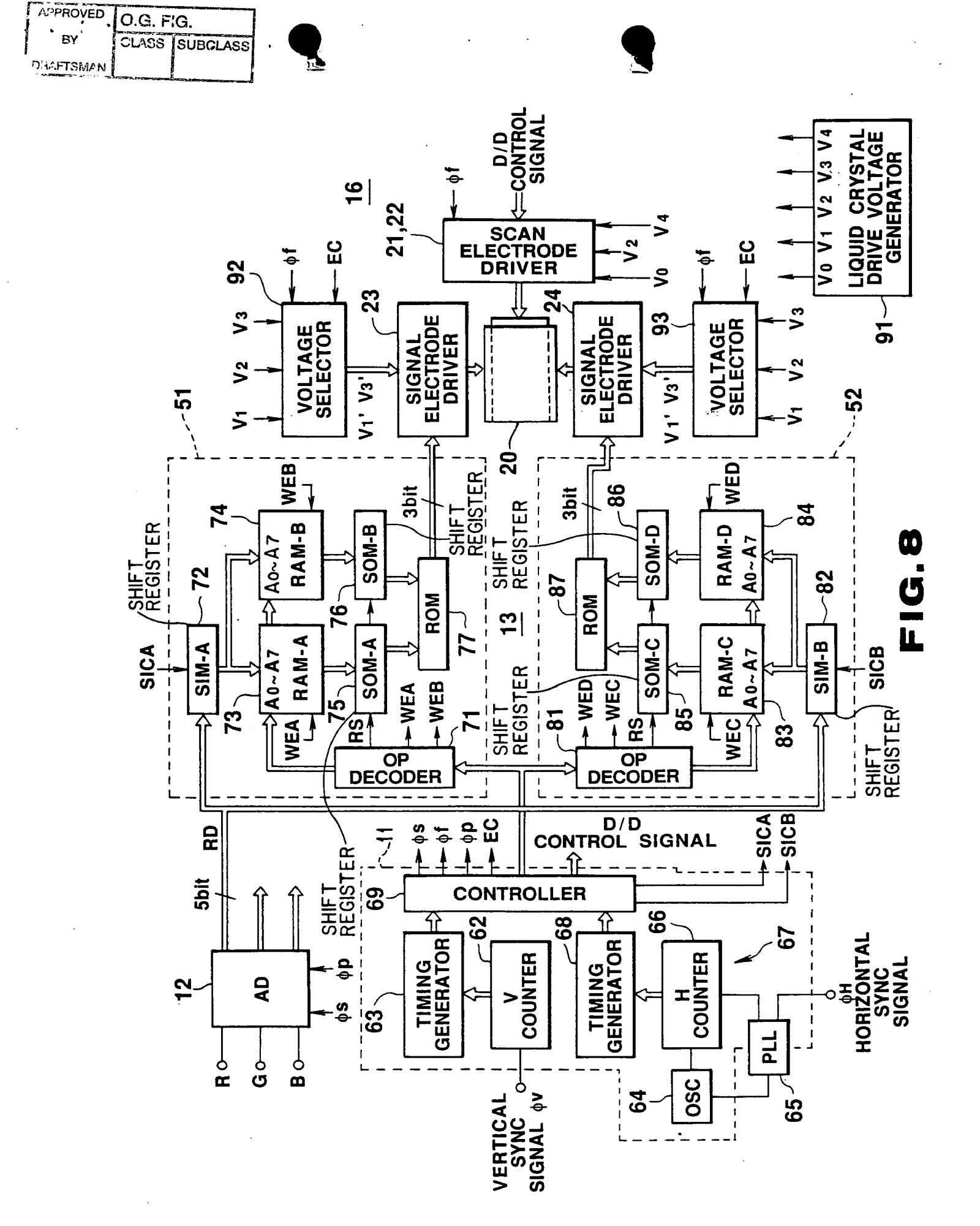


FIG.7





APPROVED O.G. FIG.

BY CLASS SUBCLASS

DRAFTOMAN

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ROM TABLE 100

CURRENT TIME TIME TIME TIME	0	-	2	က	•	15	•	3.1
0	0000	2000	3010	4110		7777		7777
1	0000	1000	2010	3110		7777		7777
2	0000	0000	1010	2110		7777		7777
3	0000	0000	0010	1110		7777		7777
•								
15	0000	0000	0000	0000		4443		7777
•								
3.1	0003	0000	0000	0000		0000		7777

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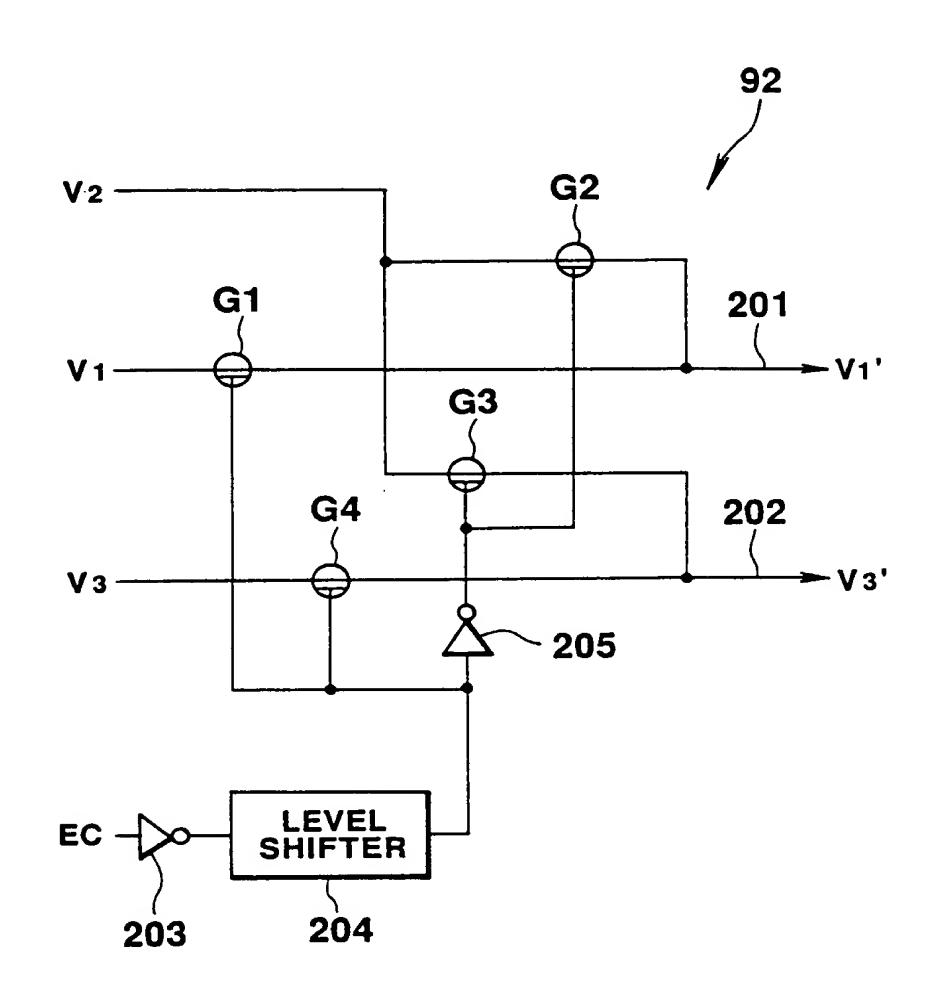
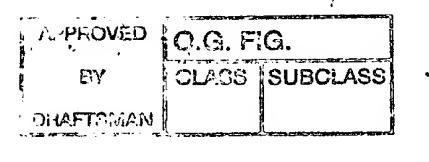


FIG. 10

	SC	SCAN ELECTRODE DRIVE SIGNAL Xn	ODE Xn	SIGN	SIGNAL ELECTRODE DRIVE SIGNAL Yn	Yn
<b>4</b>	SELECTED	NON- SELECTED STATE	ZERO BIAS EC"H"	SELECTED STATE	SELECTED SELECTED STATE	ZERO BIAS EC"H"
POSITIVE	Λo	٧2	٧2	V3	٧1	٧2
INVERTED	٧4	٧2	٧2	٧٦	٧3	٧2

# F16.11



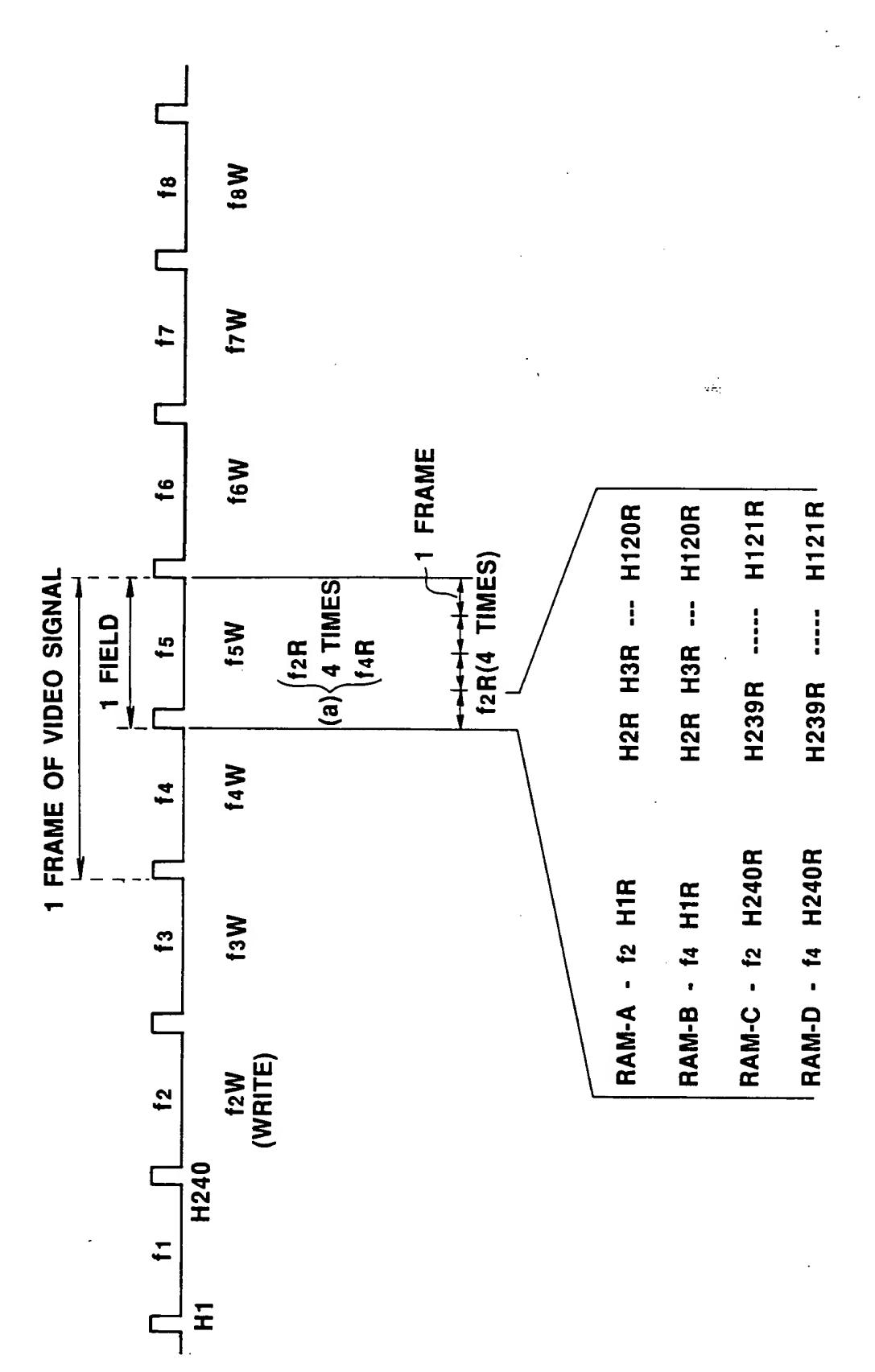


FIG. 12

DIASS SUBCLASS

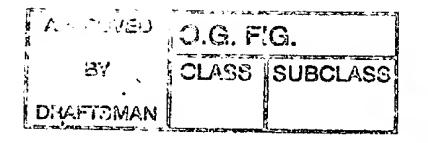
Ŧ H3 H121 <del>1</del>8 H DATA OF 1 TO SHIFT 82 Ï H123 TO RAM D WHITE CHIEF RUSIN LATCH f7H121 REG. 82 1 DATA OF TO SHIFT 72 WRITE H121 **f7** WRITE 17H1 TO RAM B WHITE DRAW BEAUT HE. H3 LATCH f7H1 T REG. 7 DATA OF TO SHIFT f6H121 H122 William Robby Hally 10 82 TO RAM C LATCH f6H121 OF WRITE SHIFT H121 **f** 1 DATA TO SHIF 72 WRITE 16H1 THE CONTRACT PRINCIPLE AND A STATE OF THE PARTY AND A STATE OF THE PART H3 LATCH f6H1 1 REG. 7 Ξ 9 F LATCH DATA O f5H121 TO SHIF REG. 82 WRITE 15H121 TO RAM C H1 ( H3 H121 ) H123 | H123 H122 OF LATCH DATA OF 15H1 TO SHIFT REG. 72 **f**5 f5H1 ALTERNATIONAL PROPERTY PROPERTY OF THE CO. TO RAM A WRITE FIG.13C SIC A FIG.13D SIC B FIG. 13F WEC FIG. 13E WEA F16.13GWEB FIG. 13H WED Η¢ **>** RS FIG.13A FIG.13B FIG.13

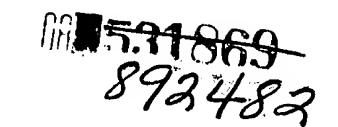
N R R R R R R (, R R R R R R R R

MARRERRERRERRER, BRRERRER REBRERRER CERRER CONTRIBUTE OF STREET OF

SOC

F1G.13J





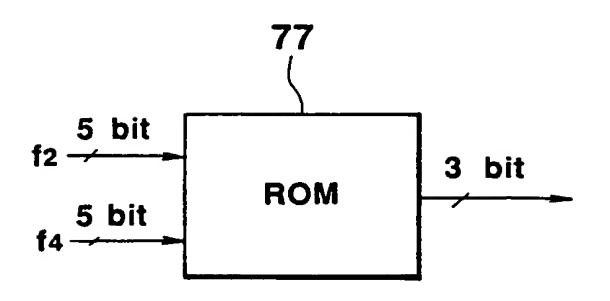


FIG.14

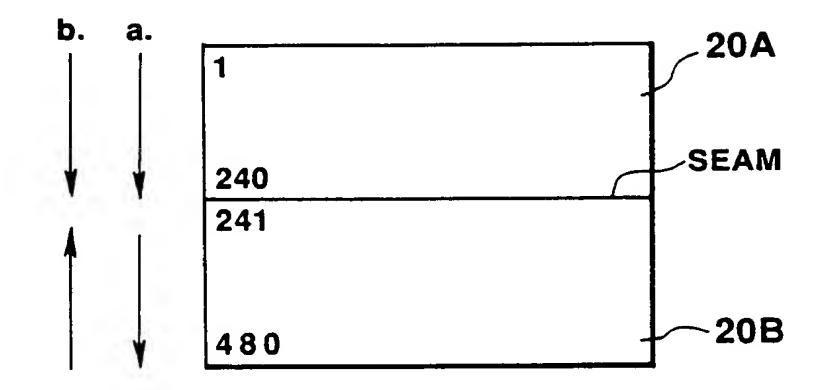
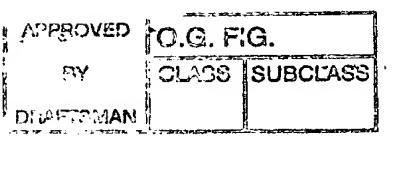


FIG.15



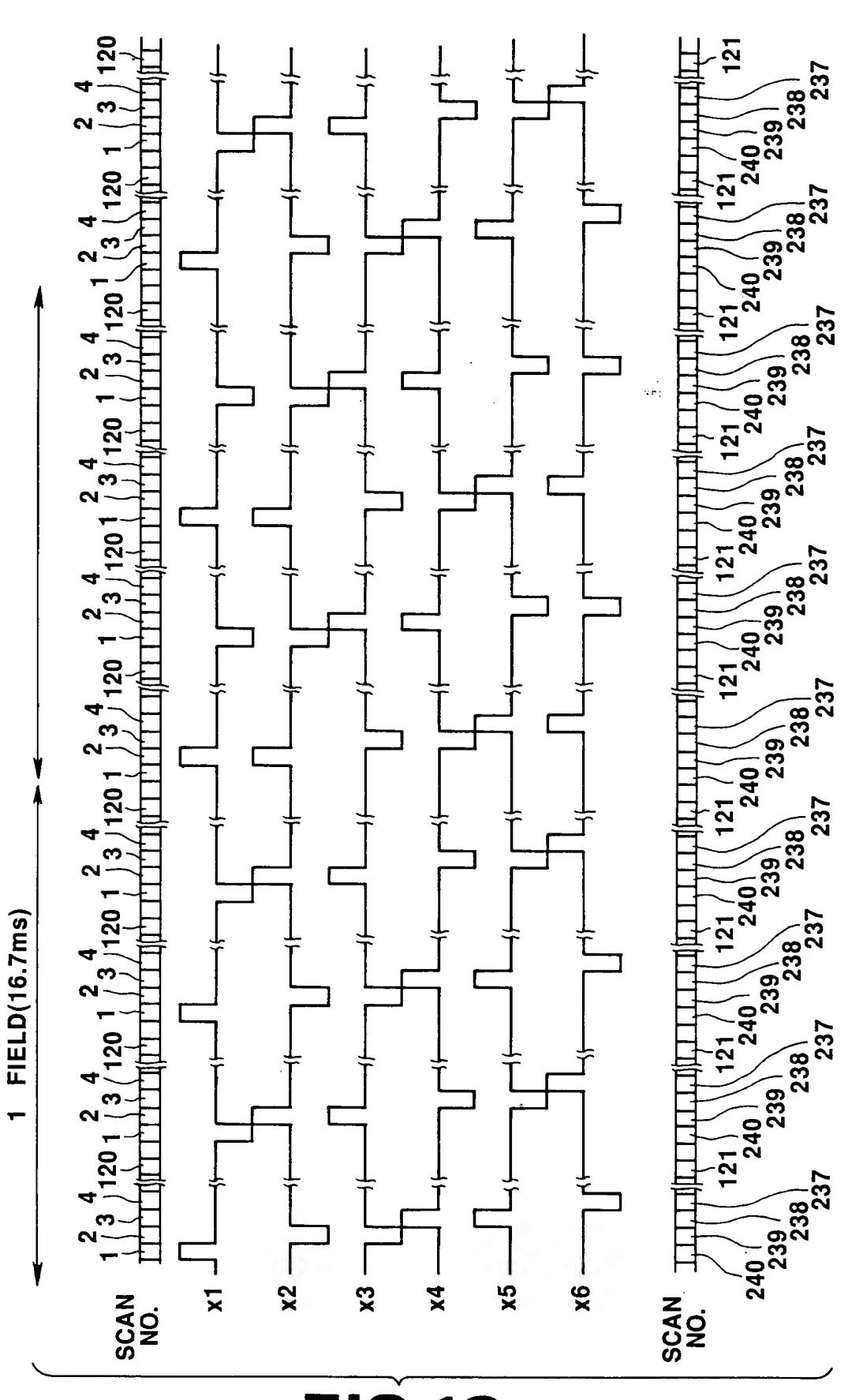
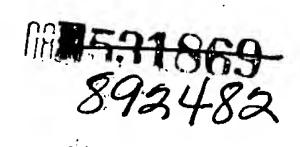
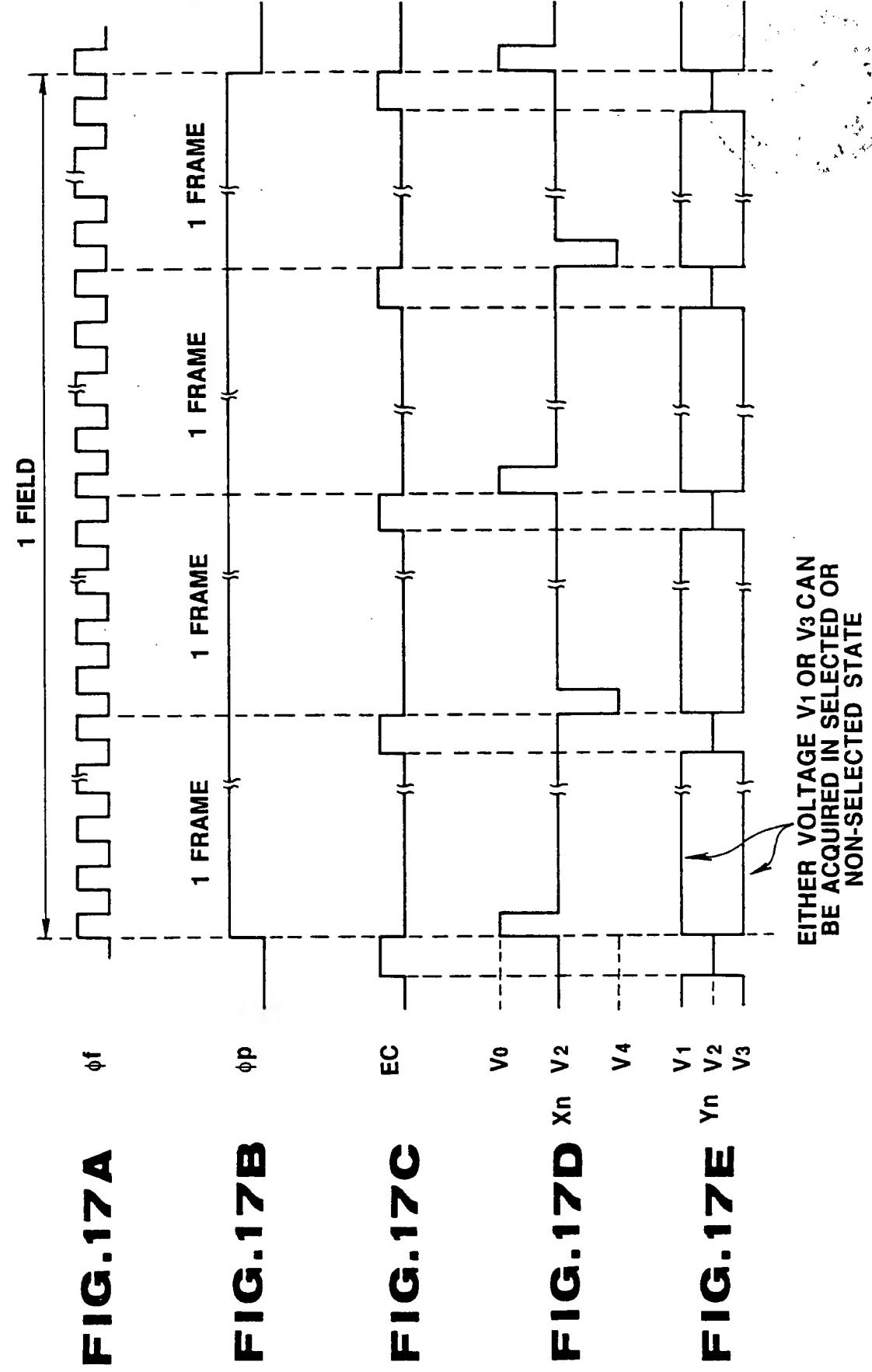
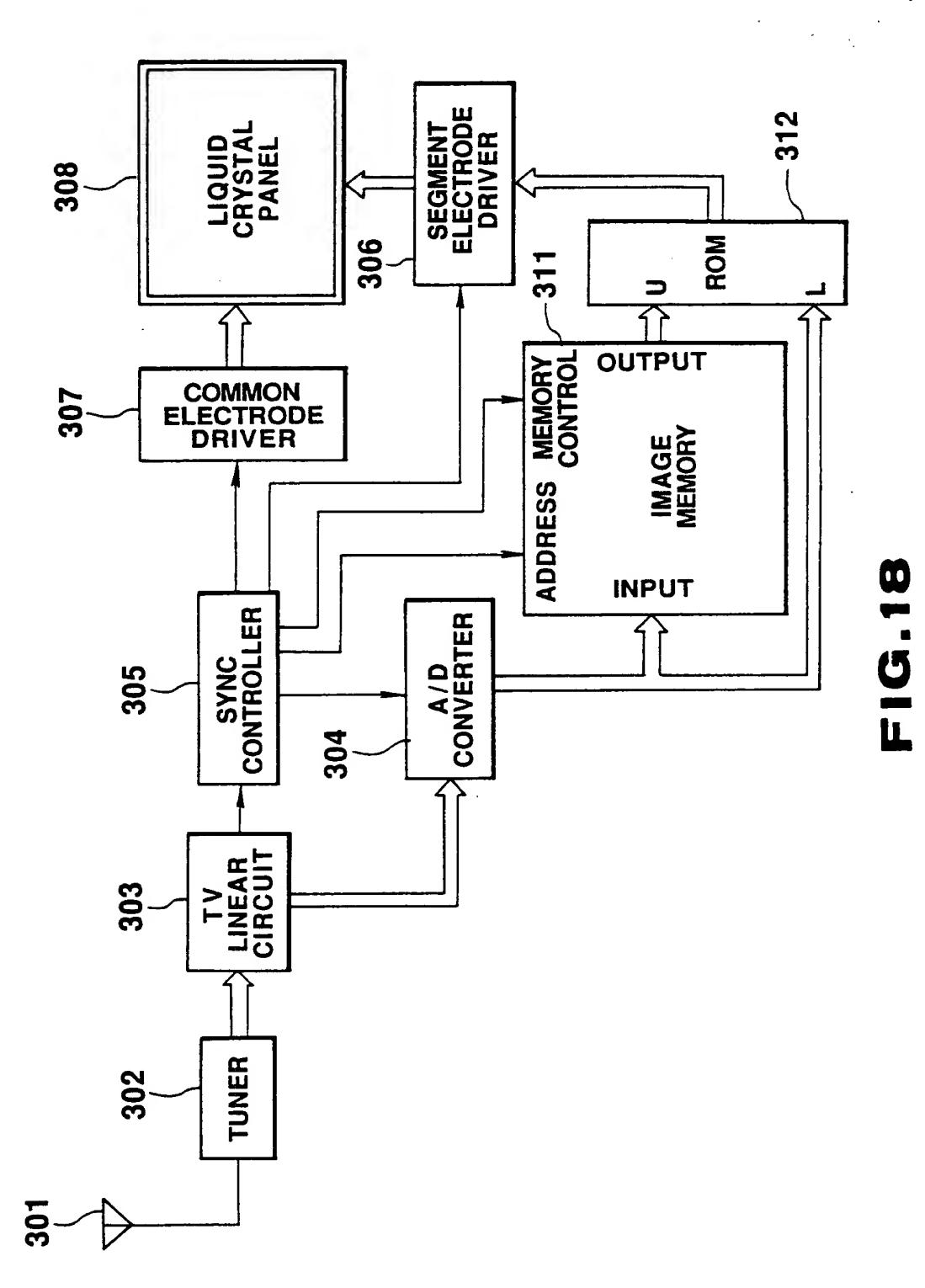


FIG.16





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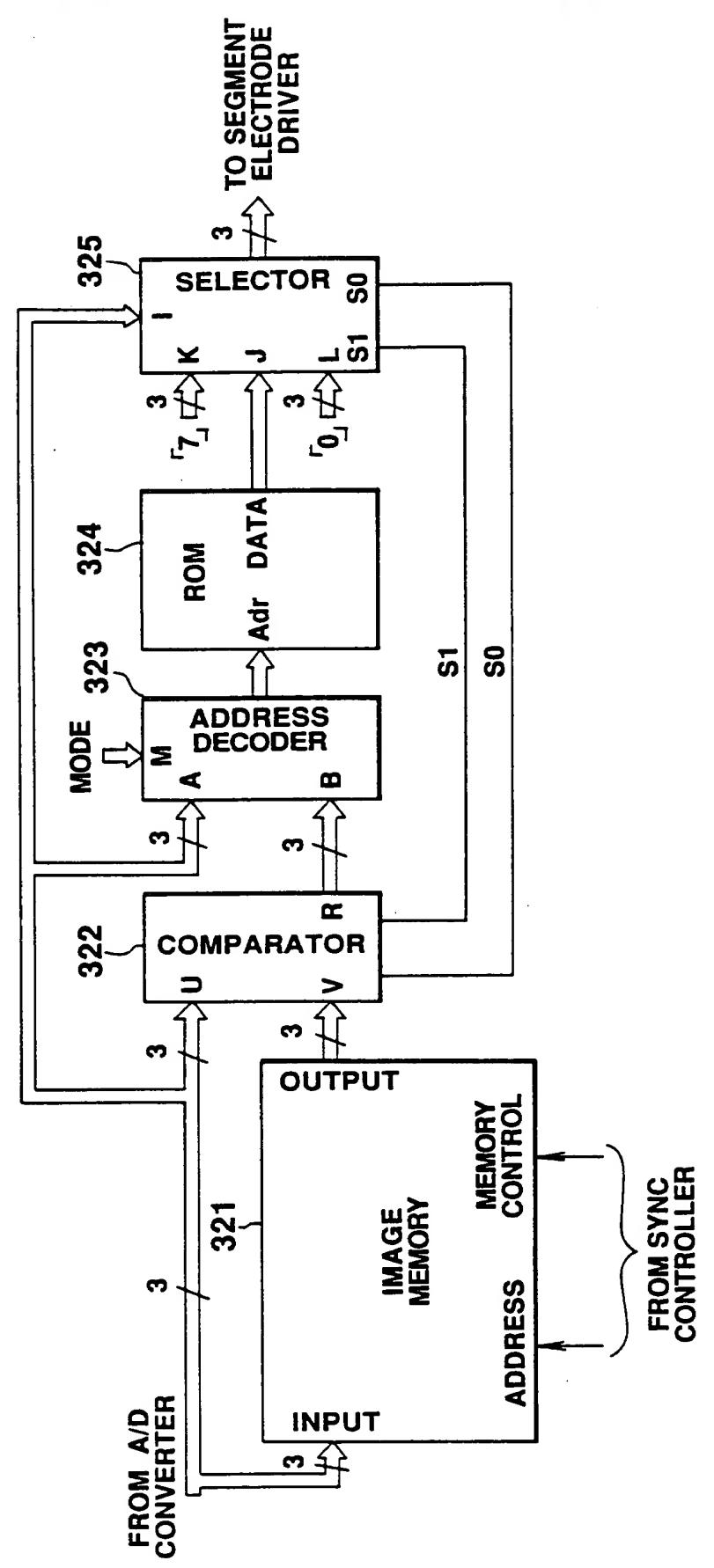


### **LOWER ADDRESSES**

·				<b>A</b> 2	0	0	0	0	1	1	1	1
				A 1	0	0	1	1	0	0	1	1
				Ao	0	1	0	1	0	1	0	1
ES	<b>A</b> 5	<b>A</b> 4	А3									
ADDRESSES	0	0	0		0	2	4	5	6	7	7	7
DRE	0	0	1		0	1	3	4	6	7	7	7
ADI	0	1	0		0	0	2	4	5	7	7	7
EB	0	1	1		0	0	1	3	5	7	7	7
UPPER	1	0	0		0	0	1	2	4	6	7	7
ר	1	0	1		0	0	0	2	3	5	7	<b>7</b> .
	1	1	0		0	0	0	1	2	4	6	7
!	1	1	1		0	0	0	0	1	3	5	7

FRAME	0	1	2	3	4	5	6	7	8	9
IMAGE DATA FROM A/D	0 —	<del>-</del> 4-	-7-	<b>-</b> 5 -	<del>-</del> 1-	<del>-</del> 3-	4	-0-	<del>-</del> 7 -	<del>-</del> 6
IMAGE DATA FROM ROM	*	<del>-</del> 6 -	<del>-</del> 7-	<del>-</del> 3 -	<del>-</del> 0 -	<del>-</del> 4 -	<del></del> 5 -	<del>-</del> 0-	<del>-</del> 7-	<del>-</del> 5

FIG.20



D. AFTERAN

U-V	<b>S</b> 1	S0	R
4 OR GREATER	1	0	*
1~3	0	1	U-V
0	0	0	*
-3~-1	0	1	U-V
-4 OR LOWER	1	1	•

0:LOW LEVEL 1:HIGH LEVEL \*:UNDETERMINED

FIG. 22

S1	S0	P OUTPUT
0	0	
0	1	.J
1	0	K
1	1	L

ĘW.

DITTERIAN





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### **CURRENT GRAY SCALE (A)**

GRAY SCALE DIFFERENCE (B)

0	1	2	3	4	5	6
+3			5	6	7	7
+2		3	5	6	7	7
+1	1	2	5	6	6	7
- 1	0	1	2	3	4	5
- 2	0	1	1	2	3	
- 3	0	0	1	1		